

# Interference

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	4	(memory same output same tim\$3 same (mode or standard or type) same (switch\$3) same (latch) same register).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/15 22:15
L4	249	(memory and output and tim\$3 and (mode or standard or type) and (switch\$3) and (latch) and register). clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/15 22:16
L5	58877	((select\$3 or arbit\$3 or arbitrat\$3) and (buffer or queue or cache)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/15 22:18
L6	81	I4 and I5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/15 22:19
L7	7488	((three or (first and second and third)) near (standard or mode or type)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/15 22:19
L8	8	I7 and I4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/15 22:20
L9	279	I7 and I5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/15 22:20